***HardwareX* article template Version 2 (June 2021)**

**Before you complete this template**, a few important points to note:

* Unsure whether your designs would make a suitable hardware article? Visit our webpage [Everything you need to know about *HardwareX*](https://www.journals.elsevier.com/hardwarex/submit-your-hardware/everything-you-need-to-know-about-hardwarex).
* The format of a hardware article is very different to a traditional research article. To help you write yours, we have created this template. **We will only consider hardware articles submitted using this template**.
* *HardwareX* is dedicated to the communication of advances in open source scientific infrastructure. **By submitting to the journal, you are confirming that all the information necessary to reproduce your design/hardware is communicated in full and is accessible for use under an open source license.**
* It is important that the designs referred to in your *HardwareX* article are **publicly available.** You’ll find information on our hardware sharing criteria in the template. Not sure how to prepare your hardware for sharing? The [Open Source Hardware Association](https://www.oshwa.org/sharing-best-practices/) has some best practice tips.
* Please consult the *HardwareX* [Guide for Authors](https://www.elsevier.com/journals/hardwarex/2468-0672/guide-for-authors) when preparing your manuscript; it highlights mandatory requirements and is packed with useful advice. We have also developed a [step-by-step video guide](https://www.journals.elsevier.com/hardwarex/submit-your-hardware/how-to-submit-your-scientific-hardware-article-to-hardwarex) to help you complete this template accurately and increase your chances of acceptance.

**Still got questions?**

* Visit our [Everything you need to know about *HardwareX*](https://www.journals.elsevier.com/hardwarex/submit-your-hardware/everything-you-need-to-know-about-hardwarex) webpage or email us at [hardwareX@elsevier.com](mailto:hardwareX@elsevier.com)

Now you are ready to fill in the template below. As you complete each section, please carefully read the associated instructions. All sections are mandatory.

***Once you have completed the template, delete this line and everything above it before submitting your article. In addition, please delete the instructions in the template (the text written in italics).***

- - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - - -

**Article title**

*Flexi-TEER, a wireless, multiplexing and computer automated trans-epithelial electrical resistance device.*

**Authors**

*Michael Anderson*

*Dennis Yaskevich*

*Jose Yeste Lozano*

*Jay Thiagarajah\**

**Affiliations**

*BCH*

*BWH*

*Wherever Dennis is*

**Corresponding author’s email address and Twitter handle**

*Jay.thiagarajah@childrens.harvard.edu*

**Abstract**

*Max. 200 words. Remember that the abstract is what readers see first in electronic abstracting & indexing services - make it brief, specific, interesting and easy to understand. If a research article refers to your hardware, cite that research article here.*

Trans-epithelial electrical resistance (TEER) measurements are typically used to quantify the degree of tight junction formation in cell monolayers. This can be used a reference point to determine when transwell monolayers are ready to be used for an experiment or can be used directly in experiments that modulate tight junctions or transcellular transport. We present here for the first time, a low-cost and accurate TEER measurement device that is capable of completely replicating the industry standard WPI’s EVOM meter and expands on its capabilities with wireless computer automation and the ability to multiplex up to 15 electrodes sequentially.

**Keywords**

* *Open source hardware*
* TEER
* 3D printing
* DIY

**Specifications table**

*Please replace the italicized instructions in the right column of the table with the relevant information about your hardware.*

|  |  |
| --- | --- |
| Hardware name | *The name of the hardware that you have invented/customized* |
| Subject area | *Please select the subject area that best reflects the community for which your hardware was developed (deleting the rest):*   * Engineering and materials science * Chemistry and biochemistry * Medical (e.g., pharmaceutical science) * Neuroscience * Biological sciences (e.g., microbiology and biochemistry) * Environmental, planetary and agricultural sciences * Educational tools and open source alternatives to existing infrastructure * General |
| Hardware type | *Please select the category that best reflects the purpose for which your hardware was developed (deleting the rest):*   * Imaging tools * Measuring physical properties and in-lab sensors * Biological sample handling and preparation * Field measurements and sensors * Electrical engineering and computer science * Mechanical engineering and materials science * Other [please specify] |
| Closest commercial analog | *Please specify the closest commercial analog to your submitted hardware. In other words, what would this hardware replace? If no commercial analog exists, state “No commercial analog is available.”* |
| Open source license | *All designs must be submitted under an open source license (for more details see the* [*Guide for Authors*](https://www.elsevier.com/journals/hardwarex/2468-0672/guide-for-authors)*). Please specify the open source license you’ve selected here.* |
| Cost of hardware | *Insert an approximate cost only - we will ask you for a complete breakdown of costs in a later section (****Bill of materials)****.* |
| Source file repository | *If you’ve uploaded your source files to an approved repository (*[*OSF*](https://osf.io/wgk7q/wiki/home/)*,* [*Mendeley Data*](https://data.mendeley.com) *or* [*Zenodo*](https://doi.org/10.5281/zenodo.3356702)*)* write the *DOI URL here.*  *For example:* <http://doi.org/10.17605/OSF.IO/WGK7Q> |
| OSHWA certification UID *(OPTIONAL)* | *We encourage you to apply for a free* [*OSHWA Certification*](https://certification.oshwa.org/)*, which confirms your work is open-source compliant.*  *If certification has been acquired, insert the OSHWA UID here. For example: “CH000005”. In your OSHWA certification project description, include a link to your HardwareX publication and the tag #HX.*  *If you haven’t acquired certification, please delete this row of the specifications table.* |

|  |  |
| --- | --- |
| Hardware name | *Flexi-TEER* |
| Subject area | *Please select the subject area that best reflects the community for which your hardware was developed (deleting the rest):*   * Electrical engineering and materials science * Medical (e.g., pharmaceutical science) * Neuroscience * Biological sciences (e.g., microbiology and biochemistry) * Educational tools and open source alternatives to existing infrastructure |
| Hardware type | *Please select the category that best reflects the purpose for which your hardware was developed (deleting the rest):*   * Measuring physical properties and in-lab sensors * Biological sample handling and preparation |
| Closest commercial analog | *World Precision Instrumentation EVOM* |
| Open source license | *All designs must be submitted under an open source license (for more details see the* [*Guide for Authors*](https://www.elsevier.com/journals/hardwarex/2468-0672/guide-for-authors)*). Please specify the open source license you’ve selected here.* |
| Cost of hardware | *$150* |
| Source file repository | *If you’ve uploaded your source files to an approved repository (*[*OSF*](https://osf.io/wgk7q/wiki/home/)*,* [*Mendeley Data*](https://data.mendeley.com) *or* [*Zenodo*](https://doi.org/10.5281/zenodo.3356702)*)* write the *DOI URL here.*  *For example:* <http://doi.org/10.17605/OSF.IO/WGK7Q> |
| OSHWA certification UID *(OPTIONAL)* | *We encourage you to apply for a free* [*OSHWA Certification*](https://certification.oshwa.org/)*, which confirms your work is open-source compliant.*  *If certification has been acquired, insert the OSHWA UID here. For example: “CH000005”. In your OSHWA certification project description, include a link to your HardwareX publication and the tag #HX.*  *If you haven’t acquired certification, please delete this row of the specifications table.* |

1. **Hardware in context**

*Write a short description of the hardware and provide context, i.e., describe similar open hardware and proprietary equipment in the field.*

Trans-epithelial electrical resistance (TEER) measurements are typically used as a proxy for degree of tight junctional formation in cell monolayer samples (ref). Normally, TEER is used to monitor the maturation of trans-well monolayer models, but it has demonstrated usages as an experimental metric in microfluidic devices as well. The standard in monitoring the maturation of cell monolayers is WPI’s EVOM meter. It is a manual device that requires the user to dunk special electrode chopsticks into each trans-well sample and manually record the resulting resistance value. Current attempts to create an open-source equivalent device of the EVOM have proven to have serious design flaws or to be unpractical (ref).

To make TEER measurements useful for experimental metrics, it needs to be computer automated. Preferably, it would be able handle many samples as well to accommodate different experimental conditions and replicates. While the EVOM unit itself is incapable of those features, an open-source add on device exists for the EVOM that allows it to be computer automated and to handle 4 samples at a time (ref). Ultimately, this is only a half measure that is still limited in the number of samples it can measure and relies on expensive proprietary equipment.

Here we present a new open-source built from the ground up TEER device that is low-cost, easy to build and does not rely on any proprietary equipment. It is capable of wireless computer automation for both triggering and recording and to handle up to 15 samples at a time. In addition, all parameters involved such as constant current, frequency and sampling time points can be altered. Construction is done via PCB manufacturing and pick and place component soldering alongside a 3D printable case. Great effort was given to making sure that the component selected were widely available and affordable.

1. **Hardware description**

*Describe your hardware, highlighting the customization rather than the steps involved in the procedure. Explain how it differs from other hardware and the advantages it offers over pre-existing methods. For example, how does this hardware compare to other hardware in terms of cost or ease of use, or how can it be used to develop further designs in a particular area?*

*Add 3-5 bullet points which broadly explain to other researchers - inside or outside of the original user community - how the hardware could help them, with either standard or novel laboratory tasks.*

Flexi-TEER is a fully contained TEER measurement device. It is possible to use it in the exact same manner as the EVOM meter is or use it in a more expanded manner with multiplexing and computer automation. If used as an EVOM alternative, we have a specific 3D printed case that allows it to look similar to it and has a built in LCD screen and on-board battery.

***Design files***

*Your design files should be editable - see* [*OSHWA’s open source definition of ‘Documentation’*](https://www.oshwa.org/definition/) *for further details. You must then either:*

* *Upload your design files to one of the three approved online repositories -* [*Mendeley Data*](https://data.mendeley.com/) *(*[*instructions*](https://doi.org/10.5281/zenodo.3346799)*), the*[*Open Science Framework*](https://osf.io/) *(*[*instructions*](https://osf.io/wgk7q/wiki/home/)*) or* [*Zenodo*](https://zenodo.org) *(*[*instructions*](https://doi.org/10.5281/zenodo.3346799)*). We recommend this option as the repositories support versioning of files.*
* *Upload your design files as supplementary materials (e.g., CAD files, videos…) to Hardware X’s online editorial system when you submit your manuscript.*
* *Include your design files in the body of the manuscript (e.g., as figures).*

*CAD files: You are encouraged to use free and open source software packages for creating the files. For CAD files,* [*OpenSCAD*](http://www.openscad.org/)*, [FreeCAD](http://www.freecadweb.org/" \t "_blank), or*[*Blender*](https://www.blender.org/)*are encouraged, but, if these are not available, we accept source files from proprietary CAD packages, such as Autocad or Solidworks, and other drawing packages.*

*3D printing. Supplementary files that facilitate digital replication of the devices are encouraged; for example, STL files for 3D printing components. We recommend uploading CAD files to the* [*NIH 3D Print Exchange*](http://3dprint.nih.gov/) *as Custom Labware and then entering the link here.*

*Electronics: PCB layouts and other electronics design files can be uploaded to the* [*Open Hardware Repository*](http://www.ohwr.org/)*or other repositories or as supplementary materials.*

*Software and firmware***:** *All software files used in the design and operation of the hardware should be included in the repository. Provide a description of the software and firmware and use extensive comments in the code.*

1. **Design files summary**

*Complete a separate row for each design file associated with your hardware (including the primary design files). Any empty rows should be deleted.*

|  |  |  |  |
| --- | --- | --- | --- |
| **Design file name** | **File type** | **Open source license** | **Location of the file** |
| *For example: Design file 1* | *e.g., CAD files, figures, videos* | *All designs must be submitted under an open hardware license. Enter the corresponding open source license for the file.* | *Either enter the URL for the repository or the sentence: "Available with the article".* |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |

*For each design file listed in the summary table above, include a short description of the file below (just one or two sentences per design file).*

***Bill of materials***

*If your bill of materials is long or complex, you can upload the details in an editable spreadsheet, e.g., ODS file type, Excel spreadsheet or PDF file, to an open access online location, such as the* [*Open Science Framework*](https://osf.io/)*repository. Include the link here. Alternatively, the bill of materials can be submitted alongside your manuscript as supplementary material.*

1. **Bill of materials summary**

*Complete a separate row for each component of your hardware – all components associated with a cost should be listed and any empty rows should be deleted.*

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| **Designator** | **Component** | **Number** | **Cost per unit -currency** | **Total cost -**  **currency** | **Source of materials** | **Material type** |
| *If possible, use the same designator here as you use in the associated design file. If that’s not possible, you will need to explain the relationship between the two.* | *Name of Component 1* | *Number of units* | *Cost per unit and the currency used* | *Total cost and the currency used* | *If possible, include a direct link to a webpage where the component can be purchased* | *Select from:*  -Metal  -Semi-conductor  -Ceramic  -Polymer  -Biomaterial  -Organic  -Inorganic  -Composite  -Nanomaterial  -Semiconductor  -Non-specific  -Other |
|  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |

*You can use this space for any additional descriptions of the materials used.*

1. **Build instructions**

*Provide detailed, step-by-step construction instructions for the submitted hardware:*

* *Include all necessary information for reproducing it.*
* *Explain and (when possible) characterize design decisions. Include any design alternatives you created.*
* *Use visual instructions such as schematics, images and videos.*
* *Clearly reference design files and component parts described in the* ***Design file summary*** *and the* ***Bill of materials summary****.*
* *Highlight any potential safety concerns.*

1. **Operation instructions**

*Provide detailed, step-by-step instructions for the safe and proper operation of the hardware.*

* *Use visual instructions, as necessary.*
* *Highlight any potential safety hazards.*

1. **Validation and characterization**

*Demonstrate the operation of the hardware and characterize its performance for a specific scientific application.*

* *Highlight a relevant use case.*
* *If possible, characterize performance of the hardware over operational parameters.*
* *Create a bulleted list describing the capabilities (and limitations) of the hardware. For example, load and operation time, spin speed, coefficient of variation, accuracy, precision, etc.*

**Ethics statements**

*HardwareX has ethical guidelines that all authors must comply with. In addition, we ask you to complete the relevant statement(s) below. Please delete those which are not relevant for your work.*

***If your work involved human subjects,*** *please include a statement here confirming that the relevant informed consent was obtained from those subjects:*

***If your work involved animal experiments,*** *please**include a statement here confirming that those experiments complied with the*[*ARRIVE guidelines*](https://www.nc3rs.org.uk/arrive-guidelines)*and were carried out in accordance with the U.K. Animals (Scientific Procedures) Act, 1986 and associated guidelines;*[*EU Directive 2010/63/EU for animal experiments*](http://ec.europa.eu/environment/chemicals/lab_animals/legislation_en.htm)*; or the National Institutes of Health guide for the care and use of laboratory animals (NIH Publications No. 8023, revised 1978). Note, the sex of the animals must be indicated, and, where appropriate, the influence (or association) of sex on the results of the study:*

**CRediT author statement**

*CRediT is in initiative that enables authors to share an accurate and detailed description of their diverse contributions to a published work.*

*Example of a CRediT author statement:*

***Michael Anderson:****Conceptualization, Methodology, Software, Writing- Original draft preparation****Dennis Yaskevich****: Visualization, Methodology, Validation,****Jose Yeste Lozano****: Methodology, Validation.****Jay Thiagarajah:*** *Supervision, Writing- Reviewing and Editing, Funding Acquisition.*

*Please add a CRediT author statement for your data article here, using the* [*categories listed on this web*](https://www.elsevier.com/authors/journal-authors/policies-and-ethics/credit-author-statement)*page.*

**Acknowledgments**

*All contributors who do not meet the criteria for authorship should be listed in an acknowledgments section.*

*In addition, please list any funding sources in this section. List funding sources in this standard way to facilitate compliance to funder's requirements:*

*Funding: This work was supported by the National Institutes of Health [grant numbers xxxx, yyyy]; the Bill & Melinda Gates Foundation, Seattle, WA [grant number zzzz]; and the United States Institutes of Peace [grant number aaaa].*

*It is not necessary to include detailed descriptions on the program or type of grants and awards. When funding is from a block grant or other resources available to a university, college, or other research institution, submit the name of the institute or organization that provided the funding.*

*If no funding has been provided for the research, please include the following sentence:*

*This research did not receive any specific grant from funding agencies in the public, commercial, or not-for-profit sectors.*

**References:**

*If relevant, you should**include a reference to the original publication of the hardware you customized and a reference to the repository in which your design files are published. Other references can be included, as required; for example, references that put your device in context in the literature. For more information on the reference format in HardwareX please see the* [*Guide for Authors*](https://www.elsevier.com/journals/hardwarex/2468-0672/guide-for-authors)*.*

\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

*Additional Information for authors.**(do not include these lines in your submission)*

***Author manuscript checklist***

* *Is the subject of the submission under an open source license? Are design files in the preferred format for making modifications as defined by the* [*Open Source Hardware definition*](http://www.oshwa.org/definition/)*?*
* *Can the hardware be reproduced with the details provided in the submission?*
* *Are all relevant design files available on either the Mendeley Data, Open Science Framework, or Zenodo repository? Are they described in the Design Files Summary, and clearly documented? (E.g., descriptive file names, commented code, labeled images, etc.)* 
  + *If in the Open Science Framework, has the repository been registered?* [*Instructions*](https://osf.io/wgk7q/wiki/home/)
  + *If in Zenodo, is the repository open access and published?* [*Instructions*](https://doi.org/10.5281/zenodo.3346799)
  + *If in Mendeley Data, is the repository published or the sharable link included in the additional information you plan to submit?* [*Instructions*](https://doi.org/10.5281/zenodo.3346799)
* *Are visual instructions used when necessary?*
* *Is the utility of the hardware to the scientific community explained clearly? Has a specific scientific application been demonstrated using the hardware?*
* *Is the performance of the hardware adequately demonstrated and characterized?*
* *Are all potential safety concerns addressed?*
* *For more information on the article template consult the* [*Guide to Authors*](https://www.elsevier.com/journals/hardwarex/2468-0672/guide-for-authors)*.*

***Reminder: Before you submit, please delete all***

***the instructions in this document (the text in italics), including this paragraph.***

***Thank you!***